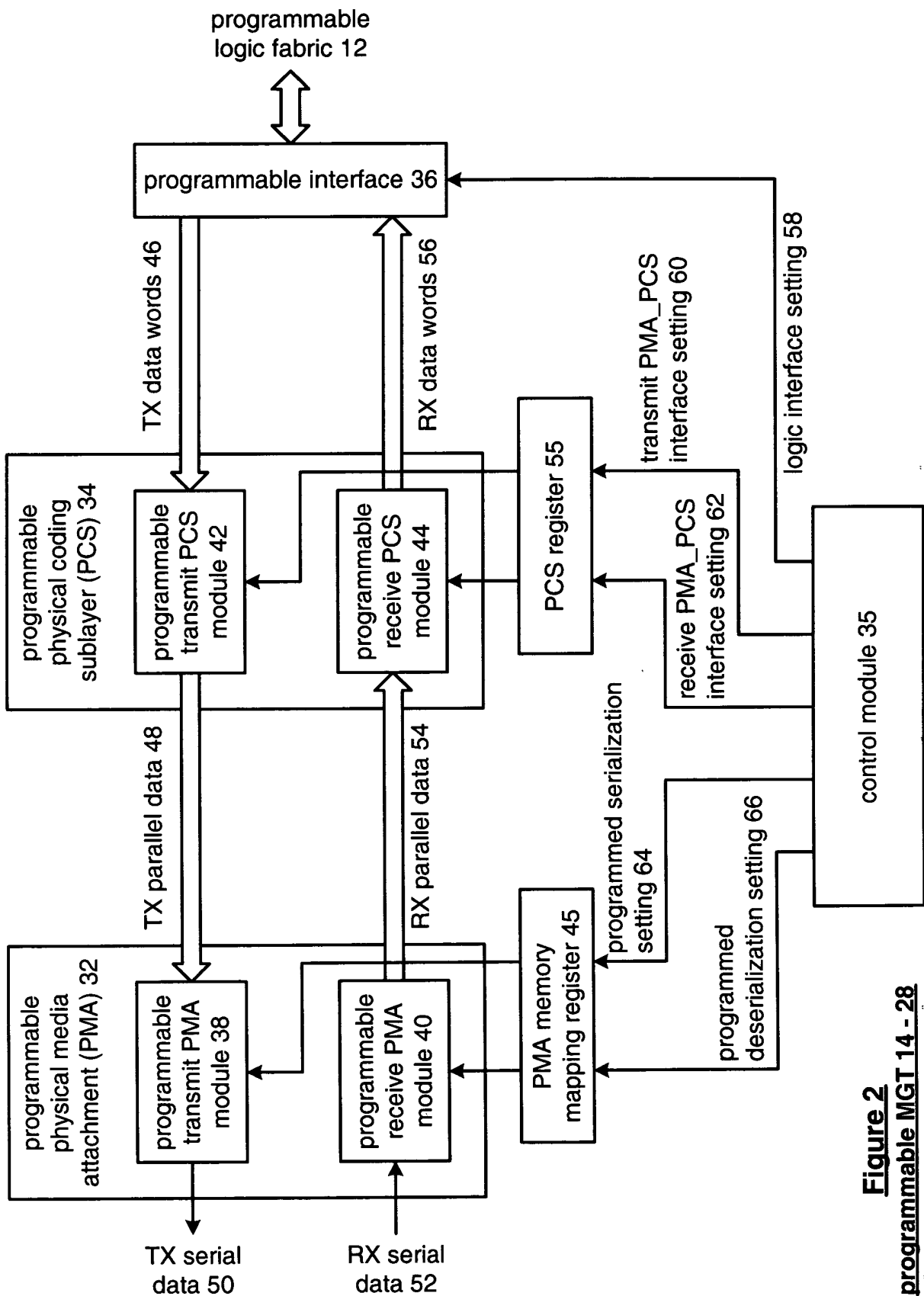
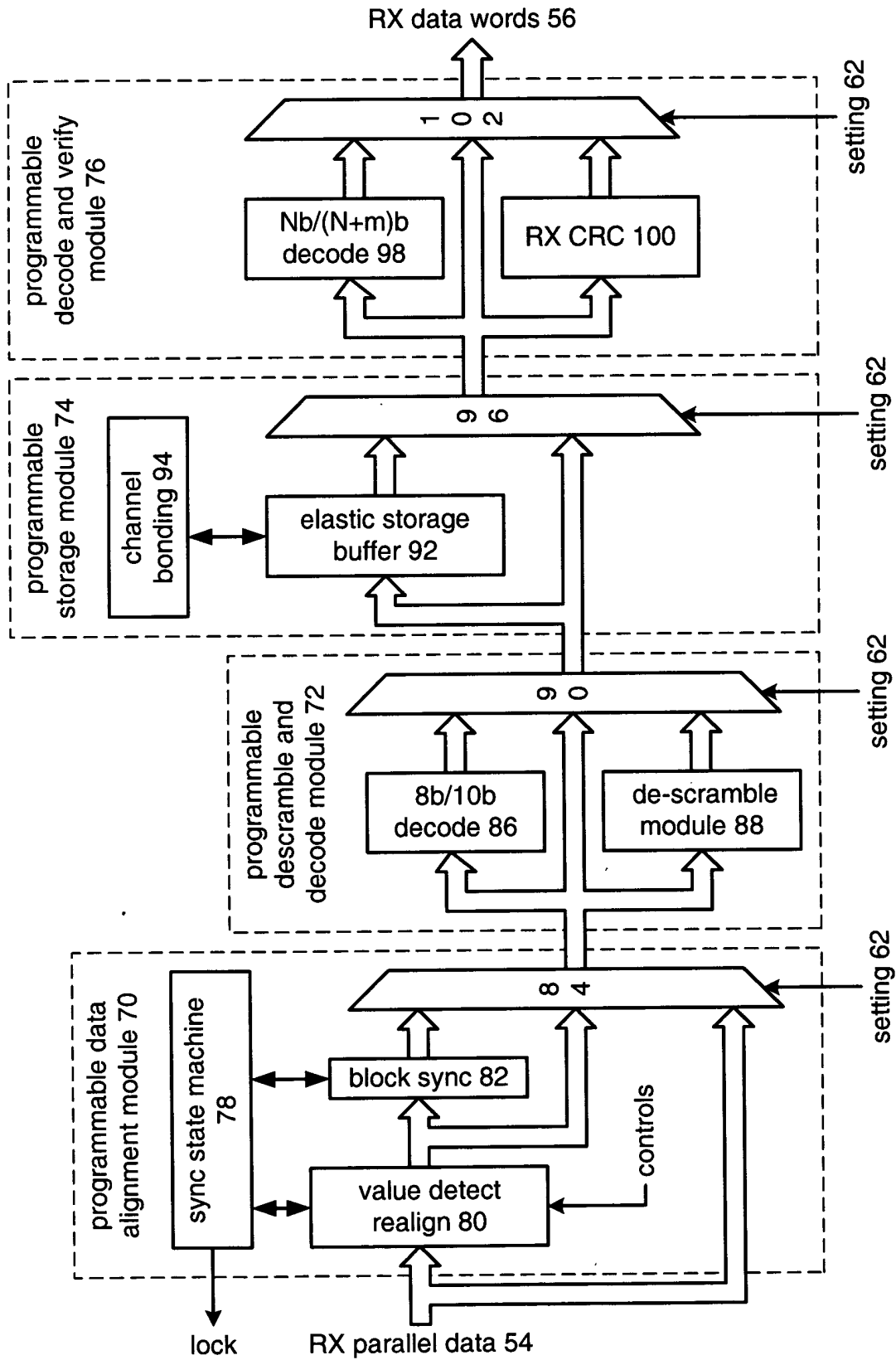


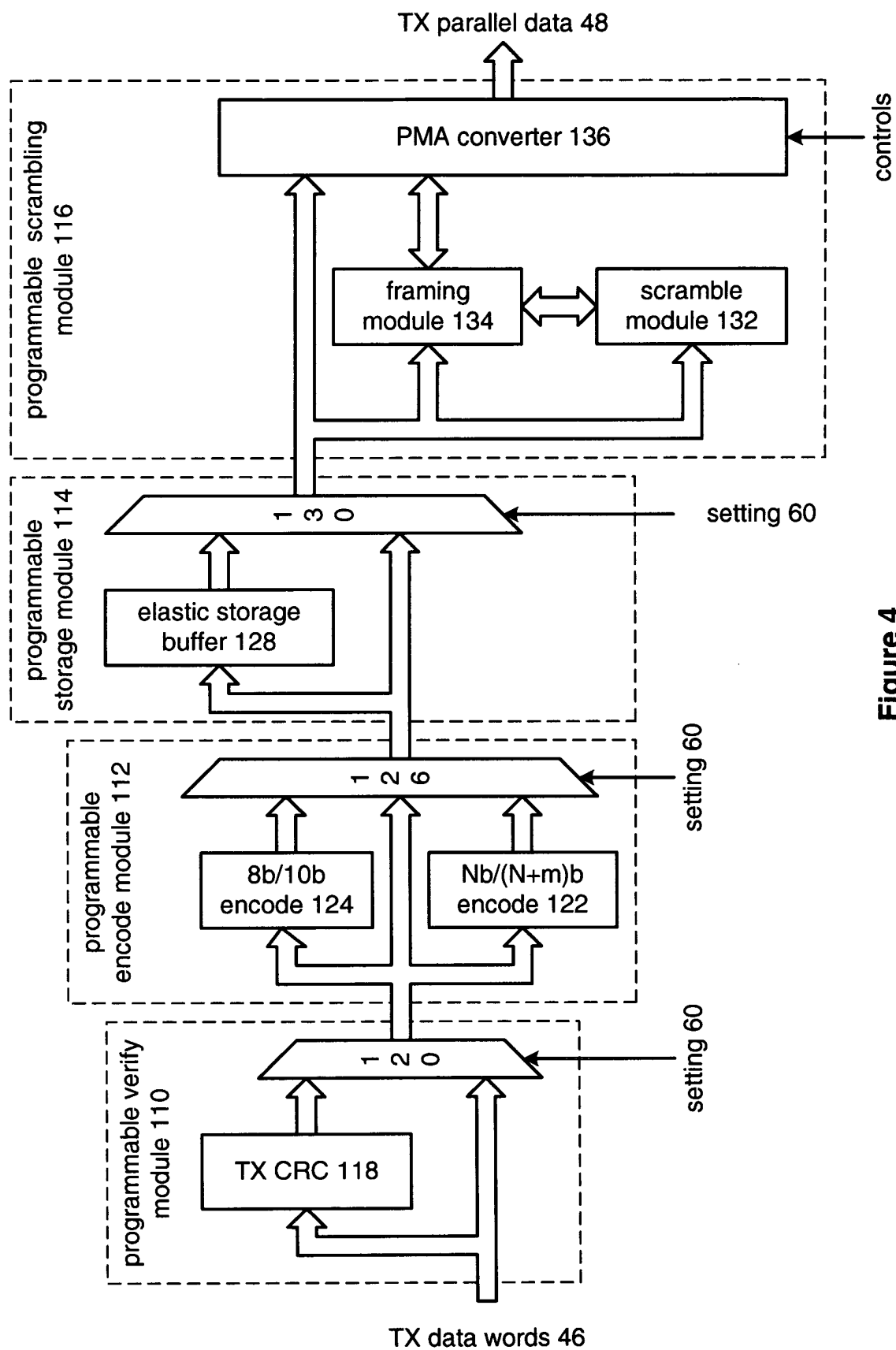
**Figure 1**  
programmable  
logic device 10



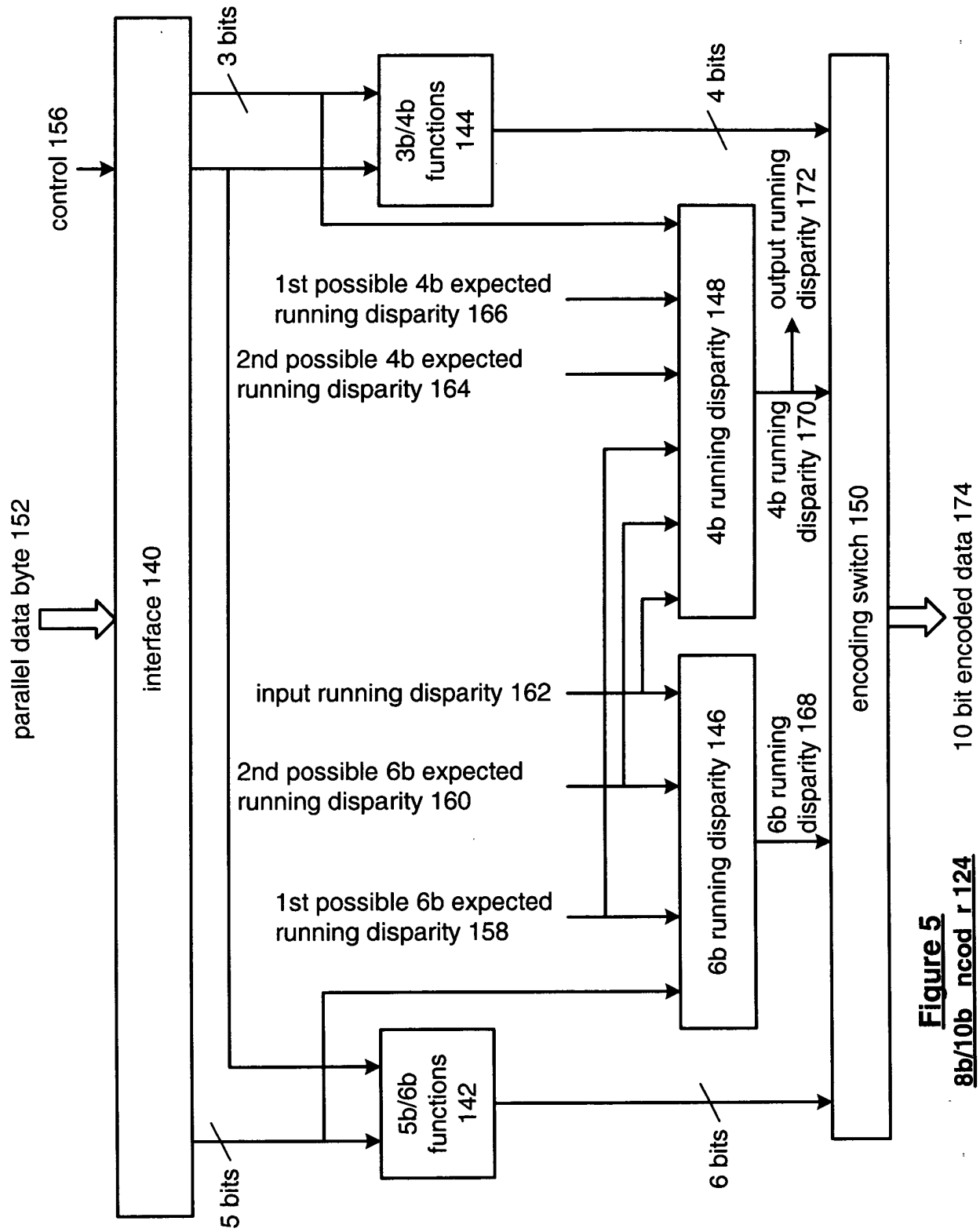
**Figure 2**  
programmable MGT 14 - 28



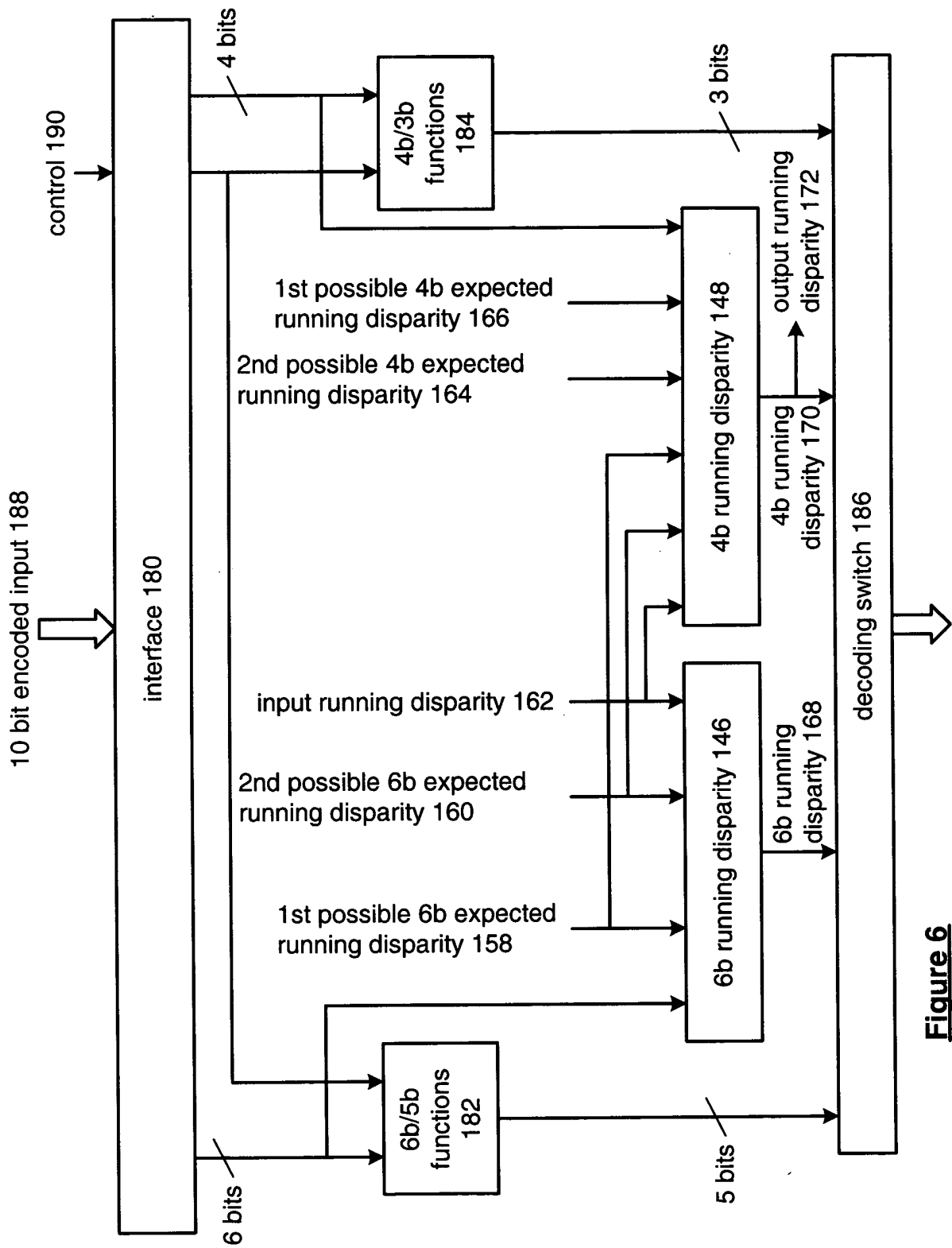
**Figure 3**  
programmable receive PCS  
module 44



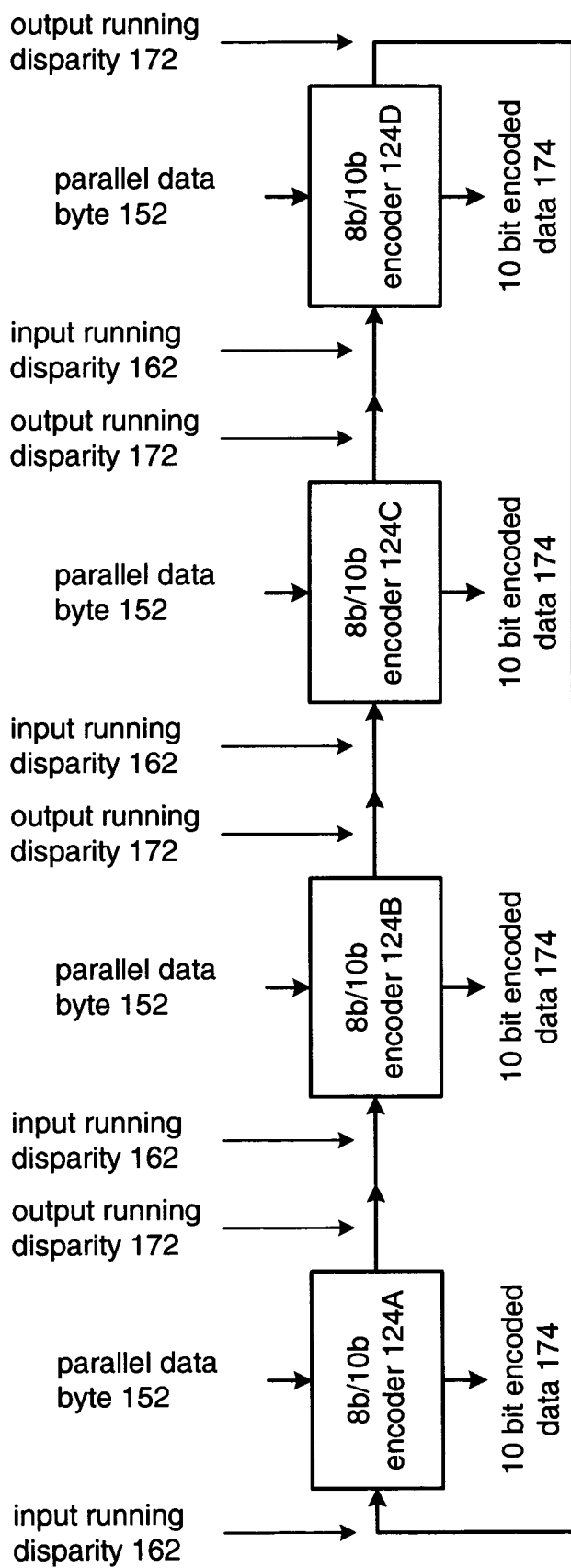
**Figure 4**  
programmable transmit PCS  
module 42



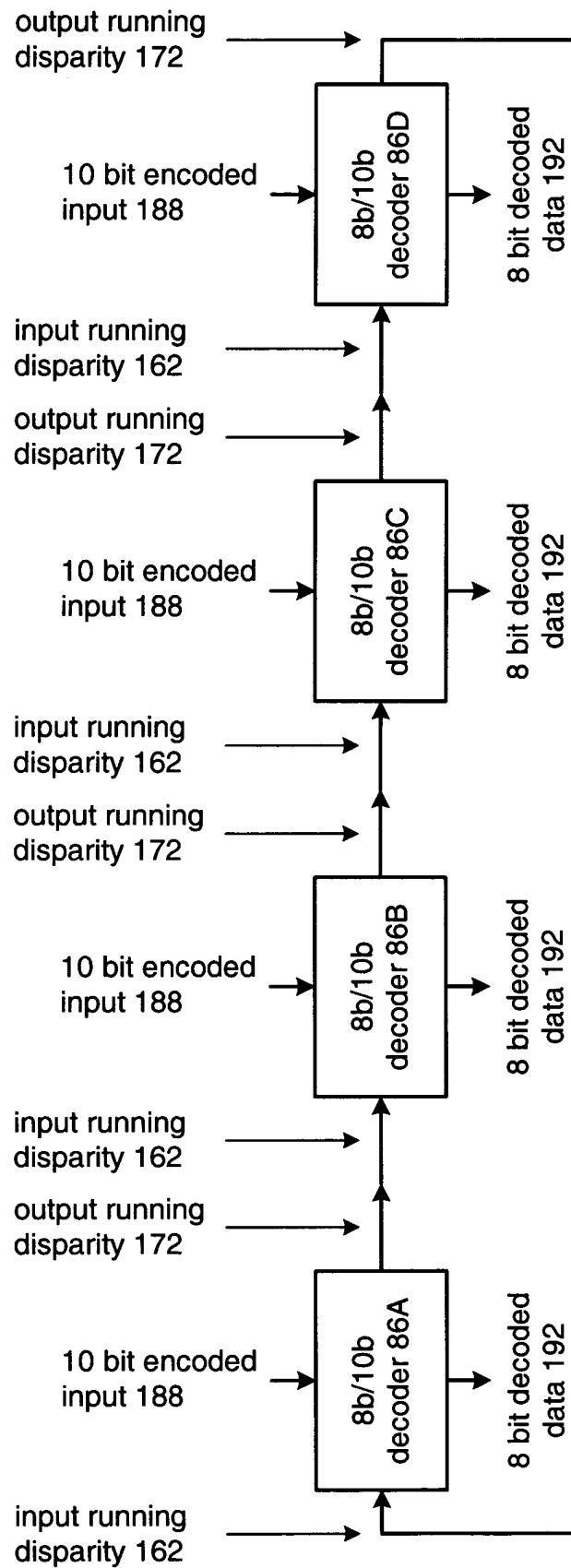
**Figure 5**  
8b/10b ncod r 124



**Figure 6**  
**8b/10b decoder 86**

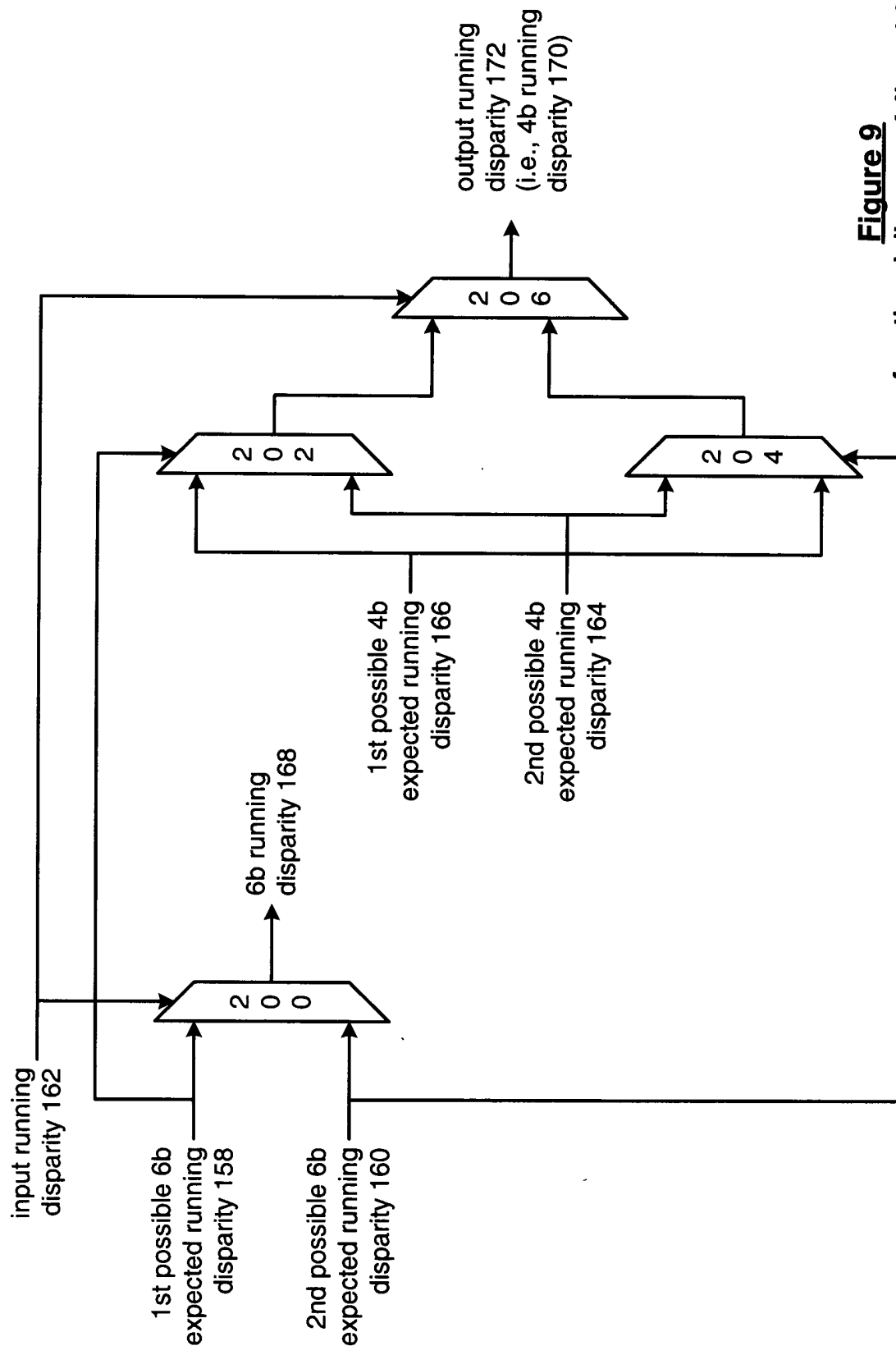


**Figure 7**  
**32b/40b encoding**

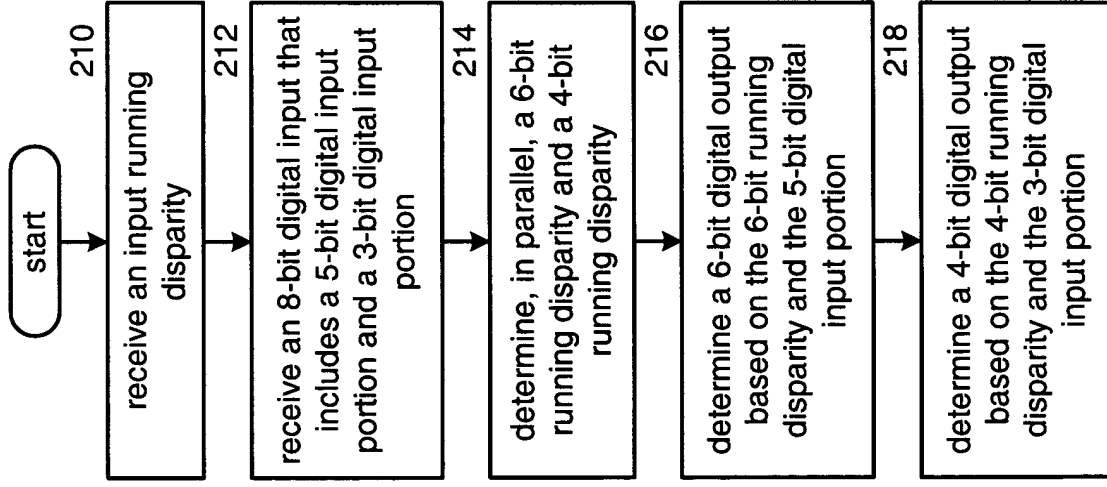


**Figure 8**  
**32b/40b decoding**

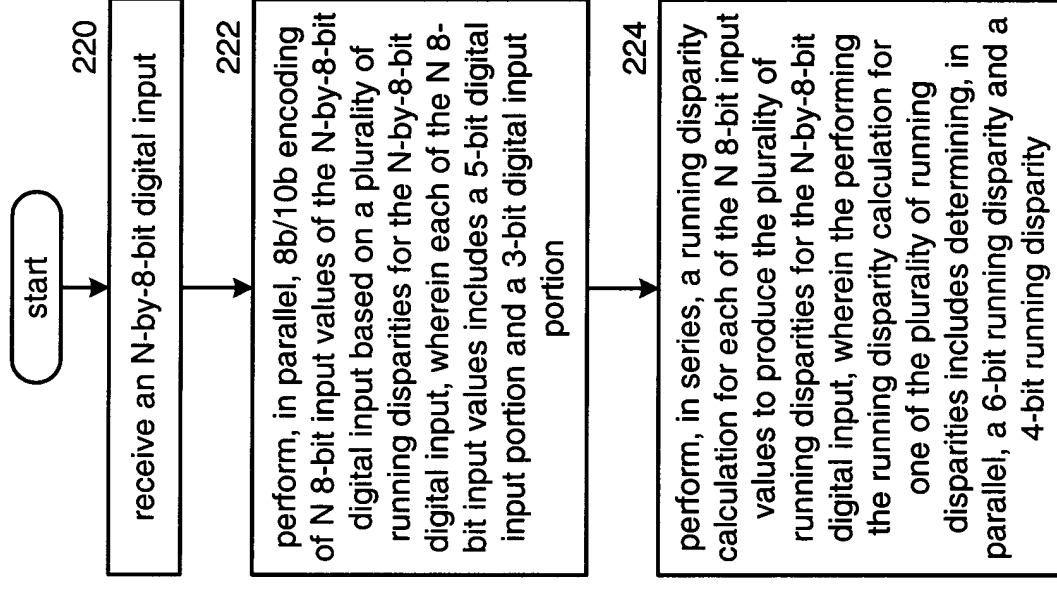




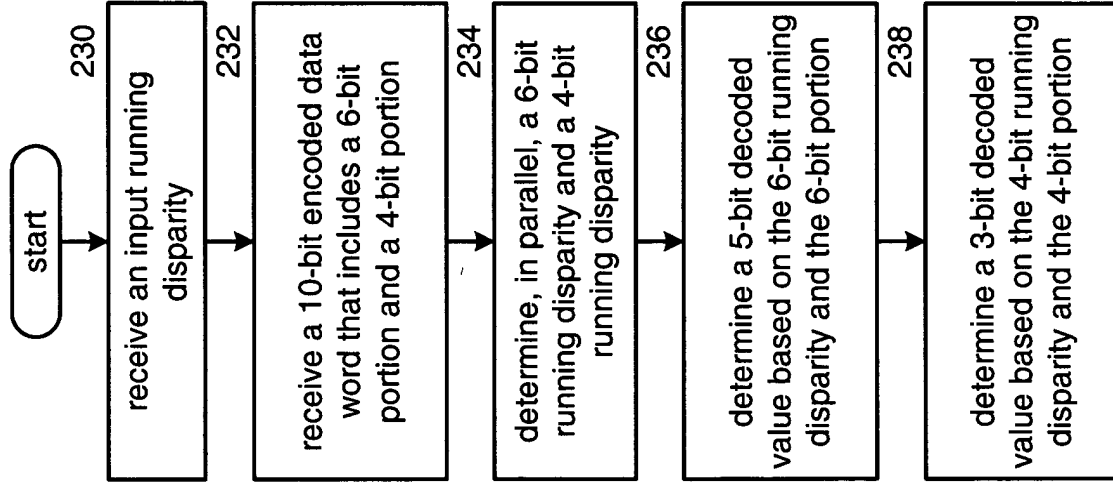
**Figure 9**  
functional diagram of 4b and 6b  
running disparities 146 and 148



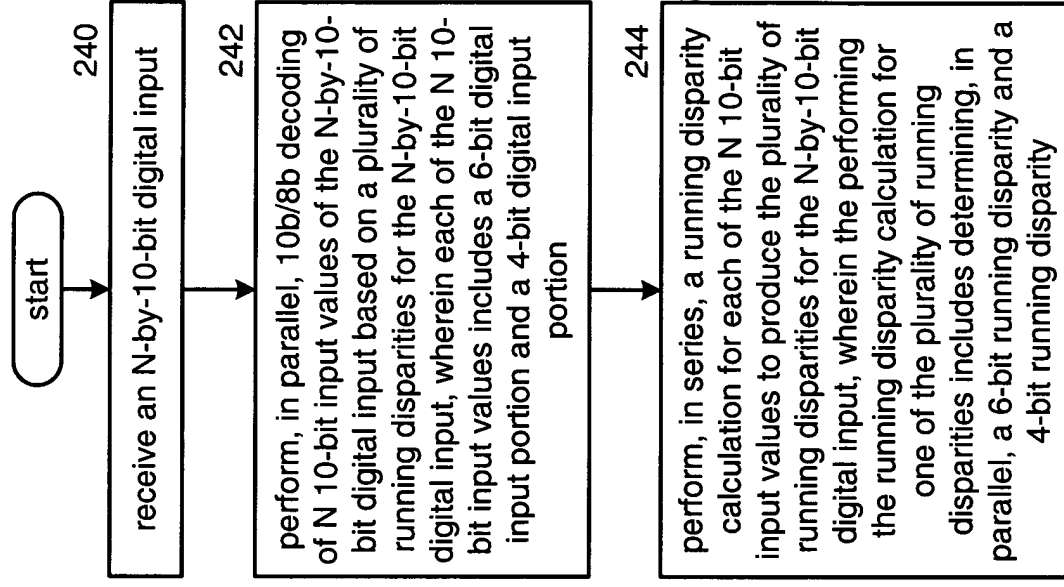
**Figure 10**



**Figure 11**



**Figur 12**



**Figure 13**